

Ref #	Hits	Search Query	DBs
L1	0	MMIC and (oscillator adj package)	US-PGPUB
L2	3	MMIC and (oscillator adj2 package)	US-PGPUB
L3	5	MMIC and (oscillator adj2 package)	US-PGPUB; USPAT
L4	0	MMIC and (oscillator adj2 package) and (reciever adj package) and (transmitter adj package)	US-PGPUB; USPAT
L5	0	MMIC and (oscillator adj2 package) and (reciever adj2 package) and (transmitter adj2 package)	US-PGPUB; USPAT
L6	0	MMIC and (oscillator adj2 package) and (receiver adj2 package) and (transmitter adj2 package)	US-PGPUB; USPAT
L7	0	MMIC and (oscillator adj2 package) and (receiver adj2 package) and (transmitter adj2 package)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
L8	141	MMIC and oscillator and receiver and transmitter and board	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
L9	27	8 and chips	USPAT
L10	58	8 and chips	US-PGPUB; USPAT; EPO; JPO; DERWENT